TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7W02F, TC7W02FU, TC7W02FK

#### **Dual 2-Input NOR Gate**

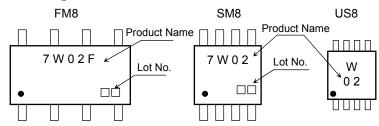
#### **Features**

High Speed : t<sub>pd</sub> = 6ns (typ.) at V<sub>CC</sub> = 5V
 Low power dissipation : I<sub>CC</sub> = 1µA (max) at Ta = 25°C
 High noise immunity : V<sub>NIH</sub> = V<sub>NIL</sub> = 28% V<sub>CC</sub> (min)

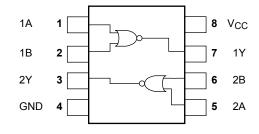
Output drive capability : 10 LSTTL Loads
 Symmetrical Output Impedance : |I<sub>OH</sub>| = I<sub>OL</sub>= 4mA (min)

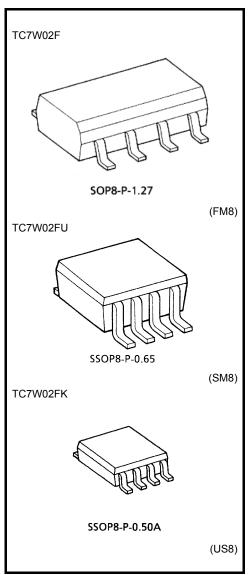
Balanced propagation delays : t<sub>pLH</sub> ≒ t<sub>pHL</sub>
 Wide operating voltage range : V<sub>CC</sub> = 2 to 6V

### Marking



### Pin Assignment (top view)





Weight

 SOP8-P-1.27
 : 0.05 g (typ.)

 SSOP8-P-0.65
 : 0.02 g (typ.)

 SSOP8-P-0.50A
 : 0.01 g (typ.)



### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	-0.5 to 7.0	V
DC input voltage	V <sub>IN</sub>	–0.5 to V <sub>CC</sub> + 0.5	V
DC output voltage	V <sub>OUT</sub>	−0.5 to V <sub>CC</sub> + 0.5	V
Input diode current	lık	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V <sub>CC</sub> /ground current	Icc	±25	mA
Dower discination	D-	300 (FM8, SM8)	mW
Power dissipation	P <sub>D</sub>	200 (US8)	IIIVV
Storage temperature	T <sub>stg</sub>	–65 to 150	°C
Lead temperature (10 s)	TL	260	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### **IEC Logic Symbol**



### **Truth Table**

Α	В	Υ
L	L	Н
L	Η	L
Н	L	L
Н	Н	L

#### **Operating Ranges**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	2.0 to 6.0	V
Input voltage	V <sub>IN</sub>	0 to V <sub>CC</sub>	٧
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	-40 to 85	°C
Input rise and fall time		0 to 1000 (V <sub>CC</sub> = 2.0 V)	
	$t_r$ , $t_f$	0 to 500 (V <sub>CC</sub> = 4.5 V)	ns
		0 to 400 $(V_{CC} = 6.0 \text{ V})$	



### **Electrical Characteristics**

### **DC Characteristics**

Characteristics Symbol		Toot	Test Condition V <sub>CC</sub> (V)			Ta = 25°C	;	Ta = -40 to 85°C		Unit
		rest			Min	Тур.	Max	Min	Max	Unit
		_		2.0	1.5	_	_	1.5	_	
High-level input voltage V <sub>IH</sub>	4.5			3.15	_	_	3.15	_		
				6.0	4.2	_	_	4.2		V
				2.0		_	0.5	_	0.5	V
Low-level input voltage V <sub>IL</sub>	$V_{IL}$	_		4.5		_	1.35	_	1.35	
				6.0		_	1.8	_	1.8	
		$V_{IN} = V_{IL}$	I <sub>OH</sub> = -20 μA	2.0	1.9	2.0	_	1.9		V
				4.5	4.4	4.5	_	4.4	_	
High-level output voltage	V <sub>OH</sub>			6.0	5.9	6.0	_	5.9	_	
			I <sub>OH</sub> = -4 mA	4.5	4.18	4.31	_	4.13	_	
			$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80	_	5.63	_	
Low-level output voltage V <sub>O</sub>		VIN = VIH	I <sub>OL</sub> = 20 μA	2.0		0.0	0.1	_	0.1	
				4.5		0.0	0.1	_	0.1	
	$V_{OL}$			6.0		0.0	0.1	_	0.1	
		0	I <sub>OL</sub> = 4 mA	4.5	_	0.17	0.26	_	0.33	
			$I_{OL} = 5.2 \text{ mA}$	6.0	_	0.18	0.26	_	0.33	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0		_	±0.1	_	±1.0	μΑ
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	_	1.0	_	10.0	μΑ

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### AC Characteristics (C<sub>L</sub>= 15pF, V<sub>CC</sub> = 5V, Ta = 25°C)

Characteristics	Symbol	Test Condition		Unit		
		rest Condition	Min	Тур.	Max	Offic
Output Transition Time	t <sub>TLH</sub>	_	_	4	8	ns
	t <sub>THL</sub>					
Propagation Delay Time	t <sub>pLH</sub>	_	_	6	12	20
	$t_{pHL}$					ns

### AC Characteristics ( $C_L = 50 pF$ , Input: $t_r = t_f = 6 ns$ )

Characteristics Sy	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	Syllibol		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
Output Transition Time $\begin{array}{c} t_{TL} \\ t_{TH} \end{array}$	4		2.0		25	75	_	95	
	tTHL	_	4.5		7	15	_	19	ns
	THL		6.0		6	13		16	
Propagation delay time	t <sub>pLH</sub>	_	2.0		25	75	_	95	
			4.5		9	15	_	19	ns
		6.0		8	13	_	16		
Input capacitance	C <sub>IN</sub>				5	10	_	10	pF
Power dissipation capacitance	$C_{PD}$		(Note 1)	_	21	_	_	_	pF

Note 1: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

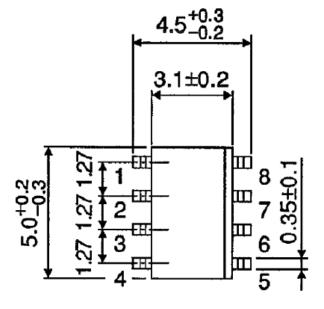
Average operating current can be obtained by the equation:

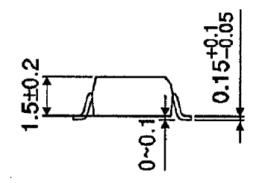
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$ 

# **Package Dimensions**

**TOSHIBA** 

SOP8-P-1.27 Unit: mm

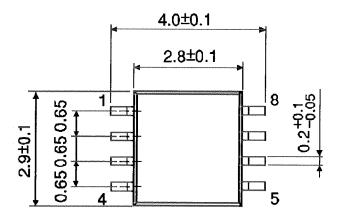


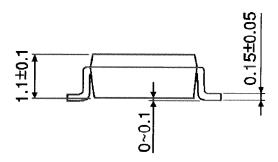


Weight: 0.05 g (typ.)

### **Package Dimensions**

SSOP8-P-0.65 Unit: mm



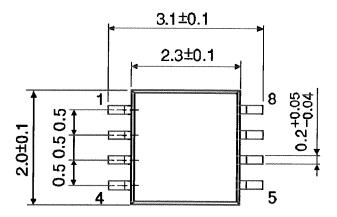


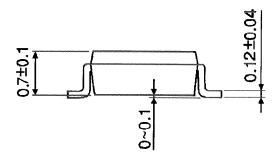
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Weight: 0.02 g (typ.)

# **Package Dimensions**

SSOP8-P-0.50A Unit: mm





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Weight: 0.01 g (typ.)

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